

FIG. 1
(PRIOR ART)

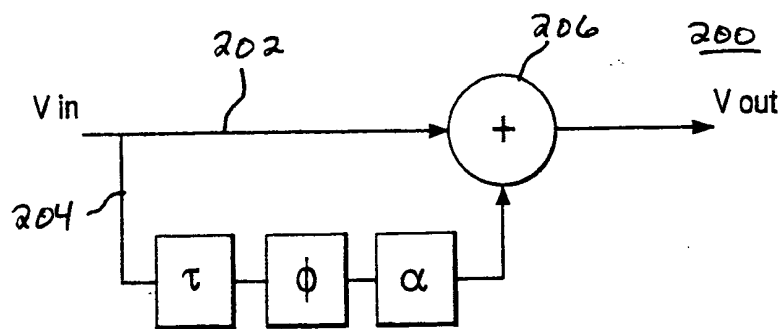


FIG. 2

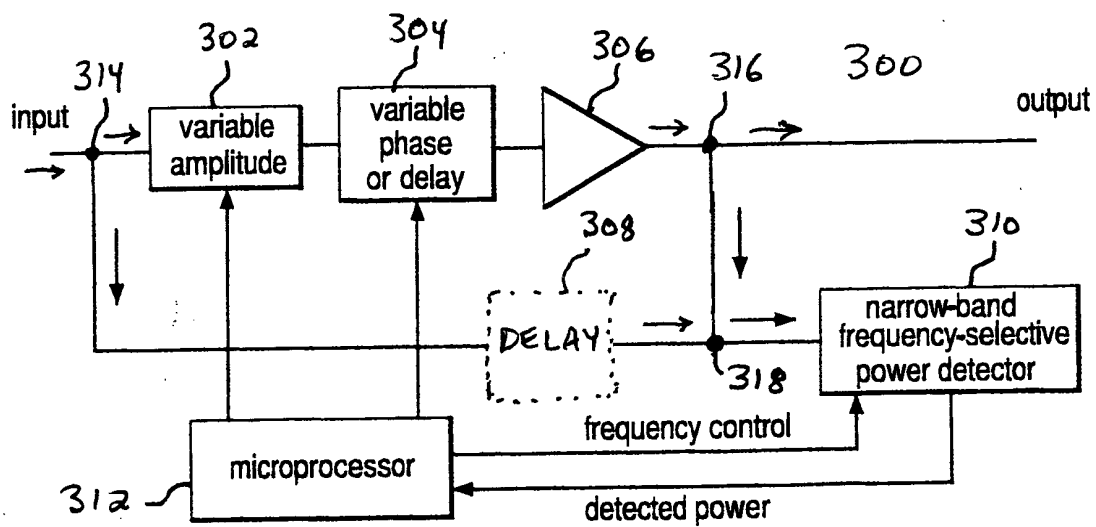


FIG. 3

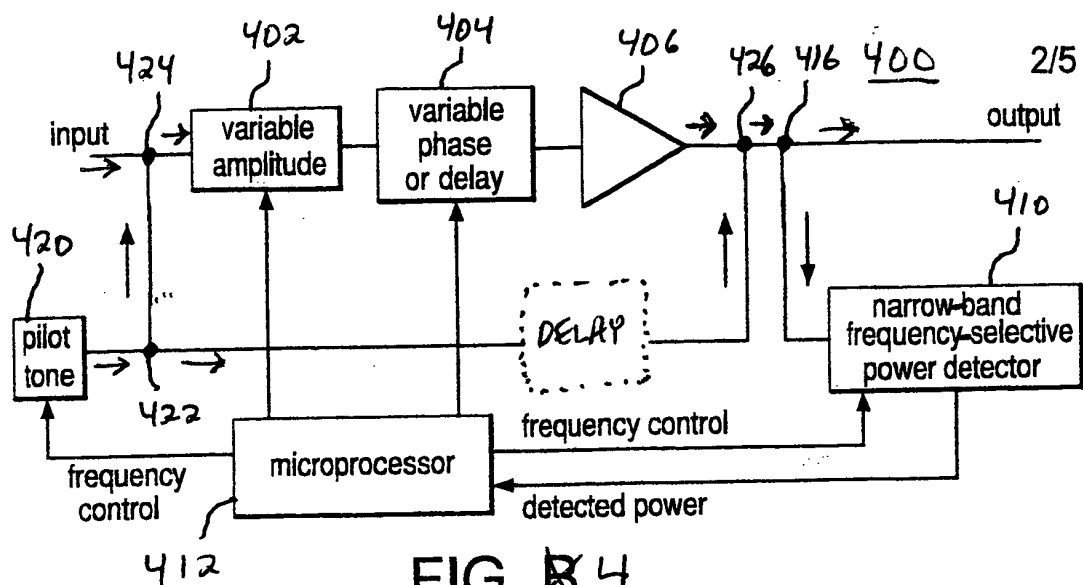


FIG. 4

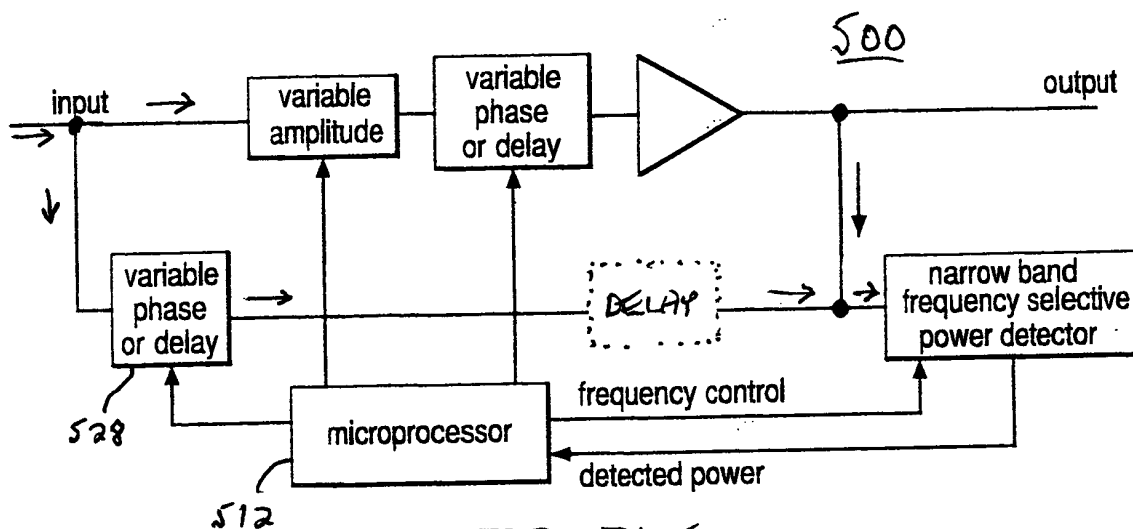


FIG. 5

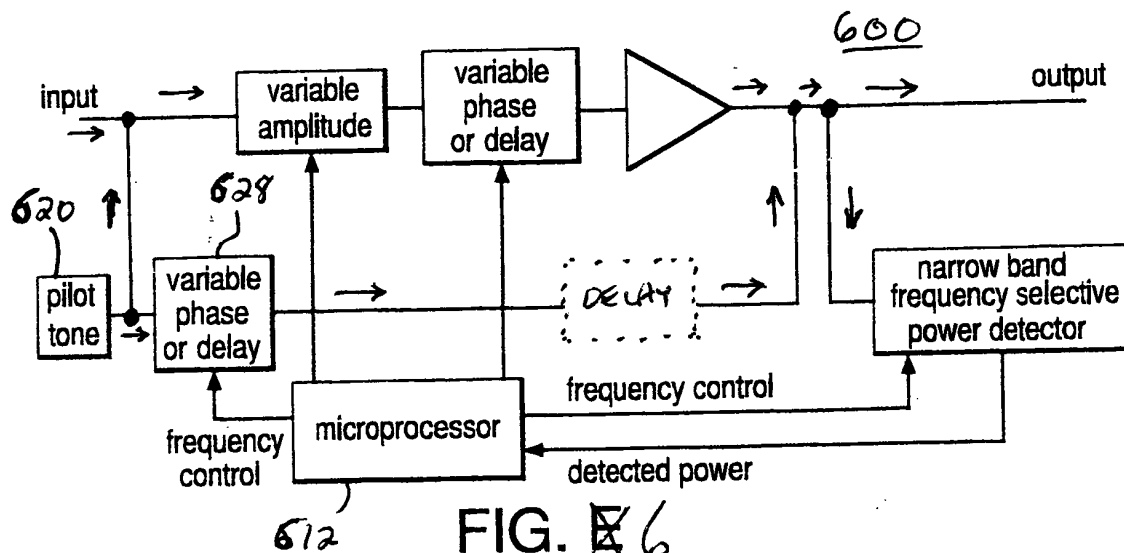


FIG. 6

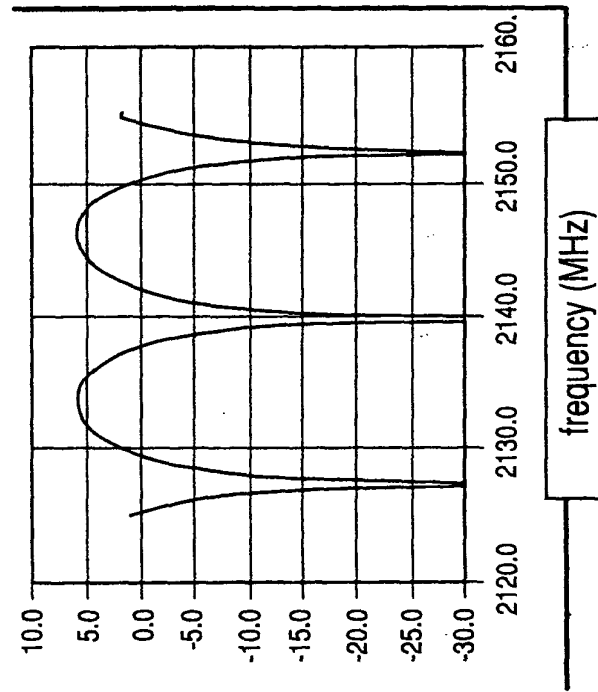
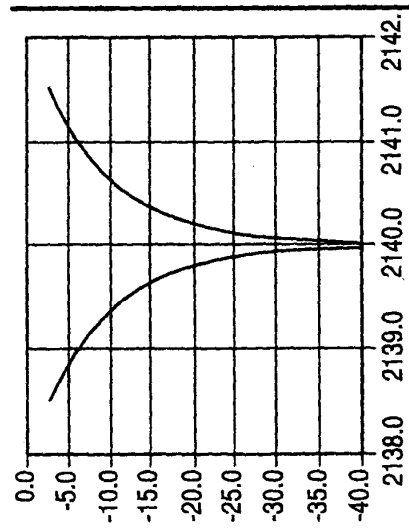
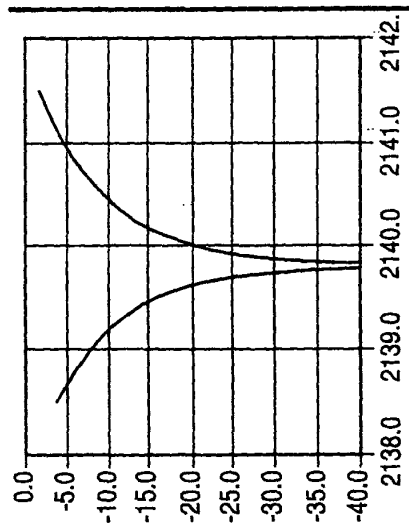


FIG. 7



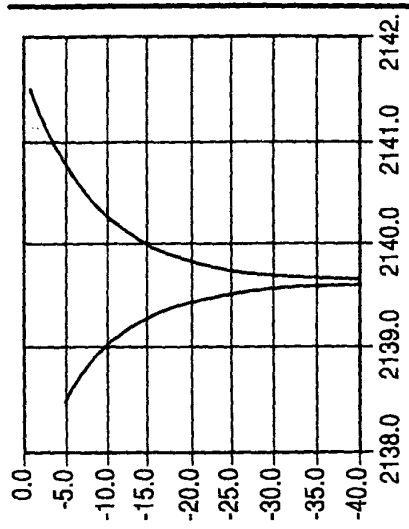
properly aligned at 2140 MHz

FIG. 8(a)



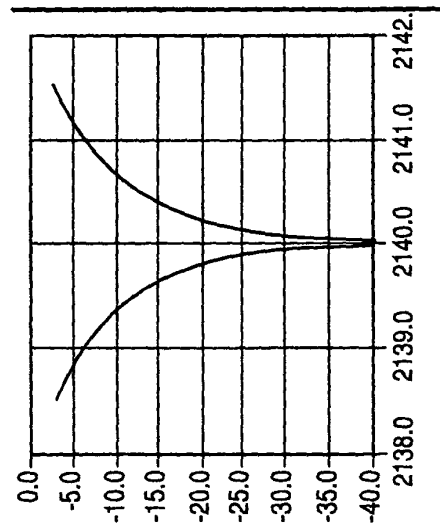
5-degree shift in phase delta

FIG. 8(b)



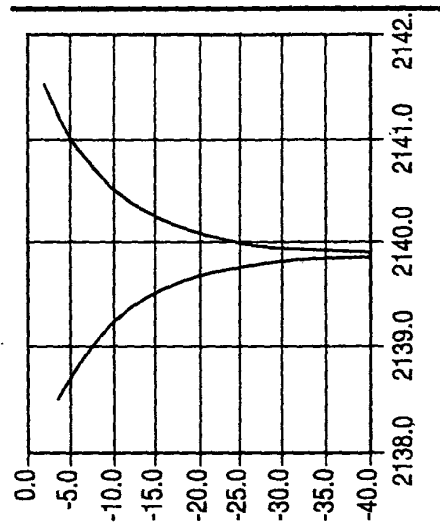
10-degree shift in phase delta

FIG. 8(c)



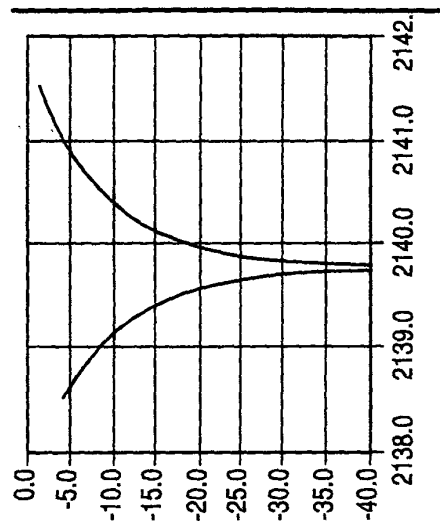
properly aligned at 2140 MHz

FIG. X_q (a)



5-pS shift in delay delta

FIG. X_q (b)



10-pS shift in delay delta

FIG. X_q (c)